



#9/Amend C
(NE)
10/25/02
C Moore

BOX AF
REPLY UNDER
37 C.F.R. § 1.116
EXPEDITED PROCEDURE

PATENT
0465-0636P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: YANG, Hae C. Conf. No.: 1780
Appl. No.: 09/452,809 Group: 2814
Filed: December 2, 1999 Examiner: Dana Farahani
For: ESD PROTECTION CIRCUIT AND METHOD FOR
FABRICATING THE SAME

AMENDMENT

BOX AF

Honorable Commissioner of Patents
Washington, D.C. 20231

October 17, 2002

Sir:

In response to the Examiner's Office Action dated July 17, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

In the Claims:

Please amend the claims as follows:

1. (Twice Amended) An ESD (Electro-Static-Discharge) protection circuit comprising:

a pad and a main chip; and,
a plurality of transistors, each connected between the pad and the main chip, said transistors having a plurality of resistors connected to an input

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